

File View Edit Tools Window Help

☐ Pending
☒ Active

- L1: (14137) memory and (bank or bl
- L2: (0) memory and (bank or block)
- L3: (0) memory and (bank or block)
- L4: (0) memory and (bank or block)
- L9: (0) (memory same (bank or bloc
- L10: (0) (memory same (bank or blo
- L11: (0) (memory same (bank or blo
- L12: (27) (memory same (bank or bl

Search: Lit Browse Queue Clear
 DBs: US-PGPUB; USPAT; EPO; JPO
 Default operator: OR

☒ Plurals
☒ Highlight all hit terms initially

BRS form ISAR form Image Text HTML

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	14137	memory and (bank or block) and (data near3 port) and terminal and global and transmitter and receiver and current and (current-mirror or (current adj mirror)) and (swing near4 voltage) and bus	US-PGPUB; USPAT; EPO; JPO
2	BRS	L2	0	memory and (bank or block) and (data near3 port) and terminal and global and transmitter and receiver and current and (current-mirror or (current adj mirror)) and (swing near4 voltage) and bus	US-PGPUB; USPAT; EPO; JPO
3	BRS	L3	0	memory and (bank or block) and (data near3 port) and terminal and global and transmitter and receiver and (current-mirror or (current adj mirror)) and (swing near4 voltage) and bus	US-PGPUB; USPAT; EPO; JPO
4	BRS	L4	0	memory and (bank or block) and (data near3 port) and terminal and global and transmitter and receiver and (current-mirror or (current adj mirror)) and swing and voltage and bus	US-PGPUB; USPAT; EPO; JPO
5	BRS	L9	0	(memory same (bank or block) same (data near3 port) same terminal same transmitter same receiver) and global and (current near3 mirror) and swing and voltage and bus	US-PGPUB; USPAT; EPO; JPO
6	BRS	L10	0	(memory same (bank or block) same (data near3 port) same terminal same transmitter same receiver) and global and current and mirror and swing and voltage and bus	US-PGPUB; USPAT; EPO; JPO
7	BRS	L11	0	(memory same (bank or block) same (data near3 port) same transmitter same receiver) and global and terminal and current and mirror and swing and voltage and bus	US-PGPUB; USPAT; EPO; JPO
8	BRS	L12	27	(memory same (bank or block) same (data near3 port)) and transmitter and receiver and global and terminal and current and mirror and swing and voltage and bus	US-PGPUB; USPAT; EPO; JPO

- ☐ Drafts
- ⌚ Pending
- ☑ **Active**
 - ☑ L3: (0) memory same (bank or block) same p
 - ☑ L4: (0) memory same (bank or block) same p
 - ☑ L5: (0) memory same (bank or block) same p
 - ☑ L6: (33) memory and (bank or block) and po
- ☑ Failed
- ☑ Saved

Search | List | **Browse** | Queue | Gear

DB9 US-PGPUB:USPAT:EPO:JPO

Default operator: **OR**

☒ Plurals

☒ Highlight all hit terms initially

 [BRS form](#) [IS&R form](#) [Image](#) [Text](#) [HTML](#)

	Type	L #	Hits	Search Text
1	BRS	L3	0	memory same (bank or block) same port same transmitter same global same bus same receiver same (current near3 mirror) same range
2	BRS	L4	0	memory same (bank or block) same port same transmitter same global same receiver and bus and (current near3 mirror) and range
3	BRS	L5	0	memory same (bank or block) same port same transmitter and global and receiver and bus and (current near3 mirror) and range
4	BRS	L6	33	memory and (bank or block) and port and transmitter and global and receiver and bus and (current near3 mirror) and range